

PRIOR ART

FIG. 1

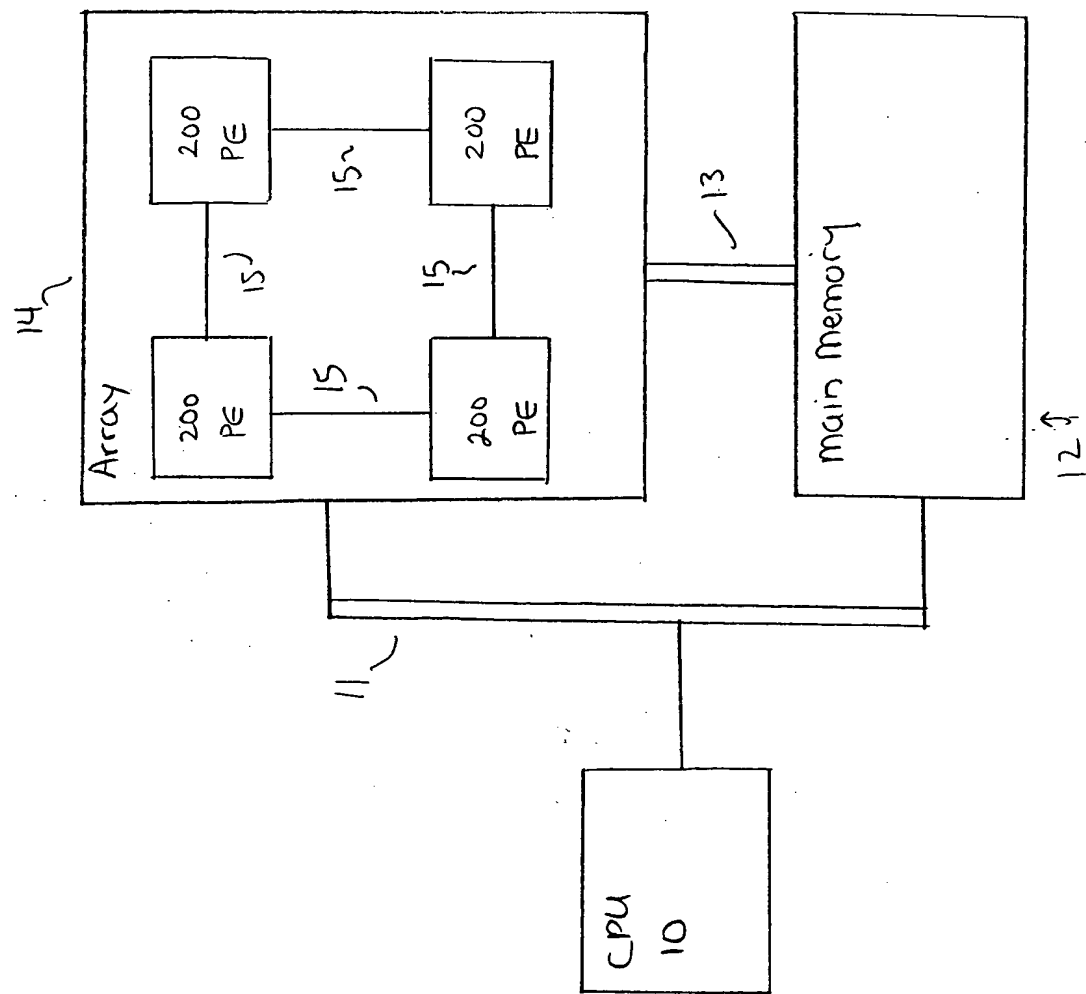


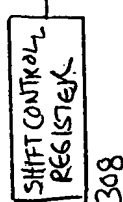
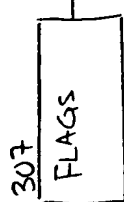
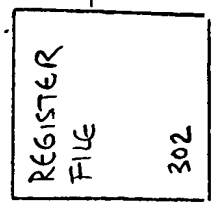
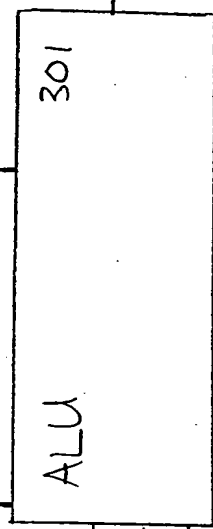
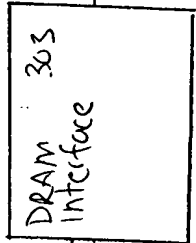
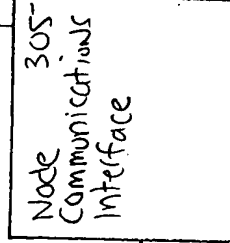
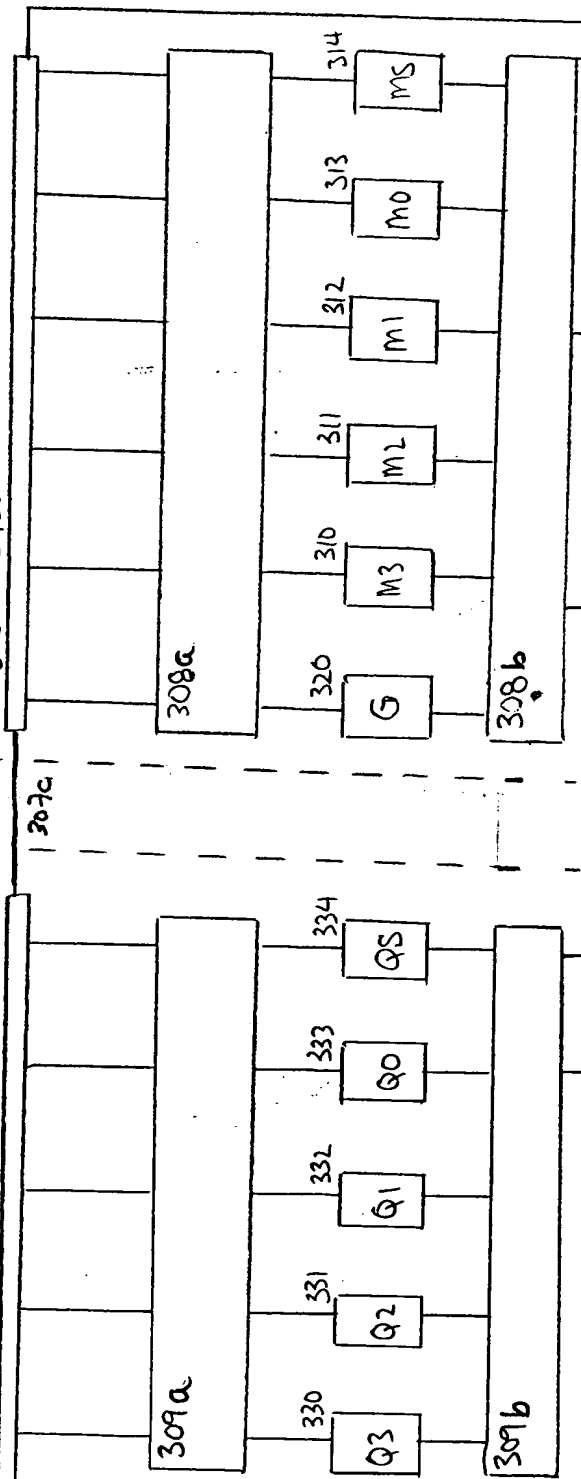
FIG. 2

Q-Block 350b

M-Block 350a

Q-Bus 307b

M-Bus 307a



306a
306b
306c
306d

304

Fig 3

FIG 4A



400

402

401

SCR bit 7
equals 1
?

Yes

Set $M2=0, M1=0, M0=0$

No

404

403

SCR bit 6
equals 1
?

Yes

Set $M2=0, M1=0, M0=0$

No

406

405

SCR bit 5
equals 1
?

Yes

Set $M2=0, M1=0, M0=0$

No

408

407

SCR bit 4
equals 1
?

Yes

Right shift by 8-bits
 $M2, M1, M0$

No

410

409

SCR bit 4
equals 1
?

Yes

Right Shift by 8-bits
 $M2, M1, M0$

412

411

SCR bit 3
equals 1
?

Yes

Right Shift by 8-bits
 $M2, M1, M0$

A

FIG 4A: 2023-06-06

FIG 4B

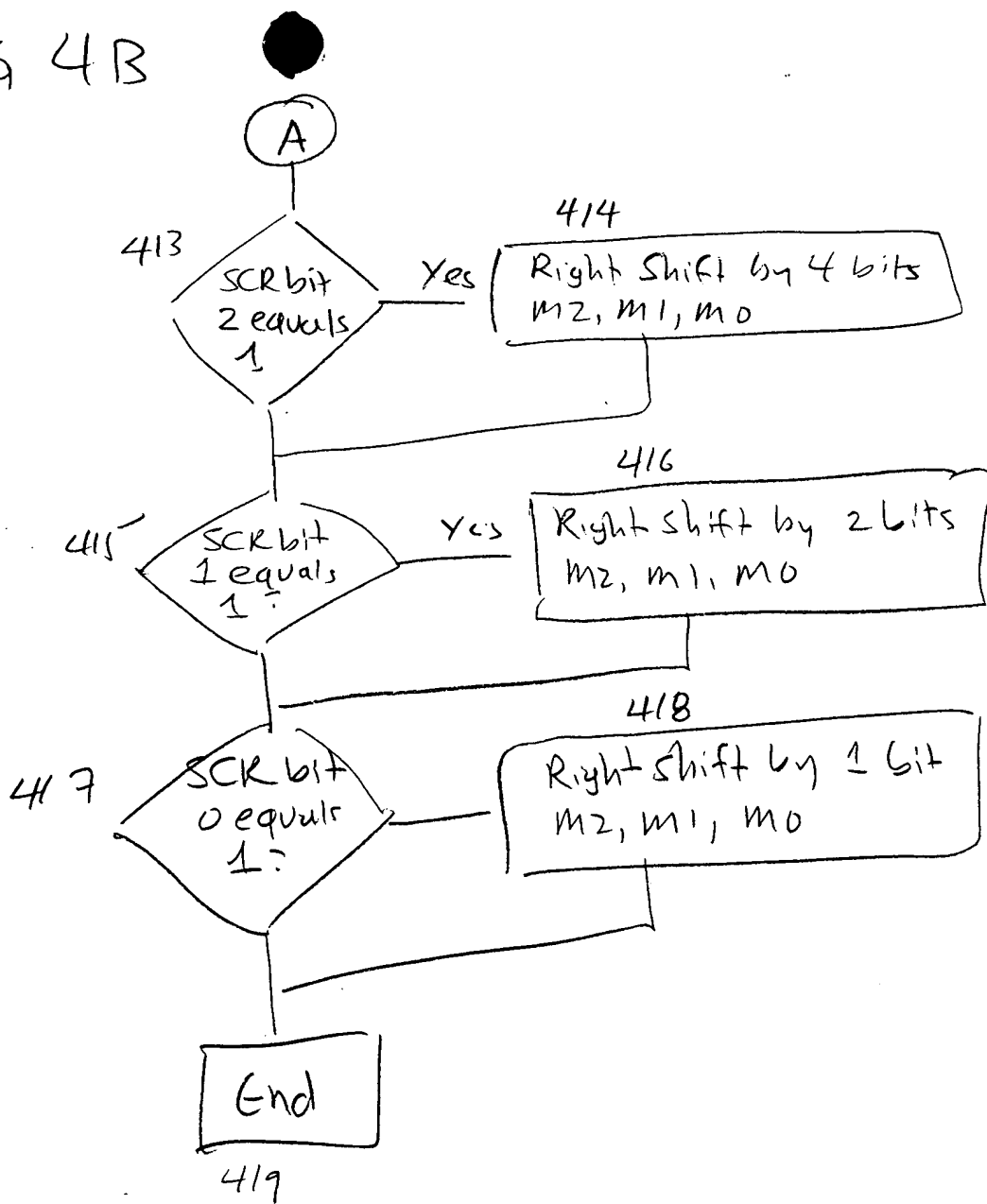


FIG 5

